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Yun He, H. Q. Ding
November 2002 **Proceedings of the Proceedings of the IEEE/ACM SC2002 Conference**

Full text available: Publisher Site   Additional Information: full citation, abstract

We investigate remapping multi-dimensional arrays on cluster of SMP architectures under OpenMP, MPI, and hybrid paradigms. Traditional method of array transpose needs an auxiliary array of the same size and a copy back stage. We recently developed an in-place method using vacancy tracking cycles. The vacancy tracking algorithm outperforms the traditional 2-array method as demonstrated by extensive comparisons. The independence of vacancy tracking cycles allows efficient parallelization of the in ...

Keywords: multidimensional arrays, index reshuffle, vacancy tracking cycles, global exchange, dynamicalremapping, MPI, OpenMP, hybrid MPI/OpenMP, SMP cluster

2 Performance evaluation: Performance characteristics of openMP constructs, and application benchmarks on a large symmetric multiprocessor
Nathan R. Fredrickson, Ahmad Afsahi, Ying Qian
June 2003 **Proceedings of the 17th annual international conference on Supercomputing**

Full text available: pdf(182.58 KB)   Additional Information: full citation, abstract, references, index terms

With the increasing popularity of small to large-scale symmetric multiprocessor (SMP) systems, there has been a dire need to have sophisticated, and flexible development and runtime environments for efficient and rapid development of parallel applications. To this end, OpenMP has emerged as the standard for parallel programming on shared-memory systems. It is very important to evaluate the performance of OpenMP constructs, kernels, and application benchmarks on large-scale SMP systems. We presen ...

Keywords: NAS OpenMP, OpenMP, SMP, SPEC OMPL2001, high-performance computing, performance evaluation

3 MPI and OpenMP paradigms on cluster of SMP architectures: the vacancy tracking algorithm for multi-dimensional array transposition
Yun He, Chris H. Q. Ding
November 2002 **Proceedings of the 2002 ACM/IEEE conference on Supercomputing**

Full text available: pdf(136.49 KB)   Additional Information: full citation, abstract, references, index terms

We investigate remapping multi-dimensional arrays on cluster of SMP architectures under

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Keyw rds: MPI, OpenMP, SMP cluster, dynamical remapping, global exchange, hybrid MPI/OpenMP, index reshuffle, multidimensional arrays, vacancy tracking cycles

4 ARMI: an adaptive, platform independent communication library
Steven Saunders, Lawrence Rauchwerger
June 2003 **ACM SIGPLAN Notices , Proceedings of the ninth ACM SIGPLAN symposium on Principles and practice of parallel programming**, Volume 38 Issue 10
Full text available: pdf(242.64 KB)   Additional Information: full citation, abstract, references, index terms

ARMI is a communication library that provides a framework for expressing fine-grain parallelism and mapping it to a particular machine using shared-memory and message passing library calls. The library is an advanced implementation of the RMI protocol and handles low-level details such as scheduling incoming communication and aggregating outgoing communication to coarsen parallelism when necessary. These details can be tuned for different platforms to allow user codes to achieve the highest perf ...

Keywords: MPI, OpenMP, Pthreads, RMI, RPC, communication library, parallel programming, run-time system

5 PACT 2001 workshops: Exploiting memory affinity in OpenMP through schedule reuse
D. S. Nikolopoulos, E. Artiaga, E. Ayguadé, J. Labarta
December 2001 **ACM SIGARCH Computer Architecture News**, Volume 29 Issue 5
Full text available: pdf(714.85 KB)   Additional Information: full citation, abstract, references, index terms

In this paper we explore the idea of reusing loop schedules to improve the scalability of numerical codes in shared-memory architectures with non-uniform memory access. The main objective is to implicitly construct affinity links between threads and data accesses and reuse them as much as possible along the execution of the program. These links are created through the definition and reuse of iteration schedules which are either defined statically by the user or created dynamically at run time. T ...

Keywords: OpenMP, computation affinity, data, page placement, shared-memory programming models

6 PACT 2001 workshops: A microbenchmark suite for OpenMP 2.0
J. Mark Bull, Darragh O'Neill
December 2001 **ACM SIGARCH Computer Architecture News**, Volume 29 Issue 5
Full text available: pdf(368.30 KB)   Additional Information: full citation, abstract, references, index terms

In this paper we present a set of extensions to an existing microbenchmark suite for OpenMP. The new benchmarks are targeted at directives introduced in the OpenMP 2.0 standard, as well as at the handling of thread-private data structures. Results are presented for a Sun HPC 6500 system, with an early access release of an OpenMP 2.0 compliant compiler, and for an SGI Origin 3000 system.

Keyw rds: OpenMP, benchmarking, shared memory

7 Bringing together automatic differentiation and OpenMP
H. Martin Bücker, Bruno Lang, Dieter an Mey, Christian H. Bischof
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Keywords: OpenMP, benchmarking, shared memory

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